

inTEST to Present at Friedman Billings Ramsey's 10th Annual Growth Conference

CHERRY HILL, N.J., May 23, 2006 -- inTEST Corporation, an independent designer, manufacturer and marketer of semiconductor automatic test equipment (ATE) interface solutions and temperature management products, will present at Friedman Billings Ramsey's 10th Annual Growth Conference on June 1, 2006 in New York. Robert E. Matthiessen, President and Chief Executive Officer, and Hugh T. Regan, Jr., Chief Financial Officer, will be presenting.

Date: Thursday, June 1, 2006
Time: 10:30 a.m., ET
Place: Grand Hyatt, New York City
Webcast: <http://www.intest.com>

About inTEST Corporation inTEST Corporation is an independent designer, manufacturer and marketer of ATE interface solutions and temperature management products, which are used by semiconductor manufacturers to perform final testing of integrated circuits (ICs) and wafers. The Company's high-performance products are designed to enable semiconductor manufacturers to improve the speed, reliability, efficiency and profitability of IC test processes. Specific products include positioner and docking hardware products, temperature management systems and customized interface solutions. The Company has established strong relationships with semiconductor manufacturers globally, which it supports through a network of local offices. For more information visit <http://www.intest.com>

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Forward-Looking Statements:

This press release includes forward-looking statements within the meaning of the Private Securities Litigation Reform Act of 1995. These statements do not convey historical information, but relate to predicted or potential future events that are based upon management's current expectations. These statements are subject to risks and uncertainties that could cause actual results to differ materially from those expressed or implied by such statements. In addition to the factors mentioned in this press release, such risks and uncertainties include, but are not limited to, changes in business conditions and the economy, generally; changes in the demand for semiconductors, generally; changes in the rates of, and timing of, capital expenditures by semiconductor manufacturers; progress of product development programs; increases in raw material and fabrication costs associated with our products; and other risk factors set forth from time to time in our SEC filings, including, but not limited to, our periodic reports on Form 10-K and Form 10-Q. The Company undertakes no obligation to update the information in this press release to reflect events or circumstances after the date hereof or to reflect the occurrence of anticipated or unanticipated events.