



## **Virage Logic to Showcase Advanced IP Solutions at Design Automation Conference in San Francisco**

### ***Company to Highlight Its Broad Portfolio of Production Proven 40nm IP Technology in Open Innovation Platform Booth***

FREMONT, Calif., Jul 21, 2009 (BUSINESS WIRE) -- [Virage Logic Corporation](#) (NASDAQ:VIRL), the semiconductor industry's trusted IP partner, announced it will be a featured partner in [TSMC's Open Innovation Platform](#)(TM) (OIP) booth (#822), at the Design Automation Conference ([DAC](#)) being held July 27-30, 2009, at the Moscone Convention Center in San Francisco, California.

Virage Logic will be showcasing its production proven 40-nanometer (nm) products that are designed to optimize area, performance, power and yield. Learn how the Company is leading the way on the [Sure Path to 40nm Success](#).

In addition to exhibiting at DAC, Virage Logic will be participating on a number of interactive panels and informative sessions where conference attendees can learn more about the Company's extensive IP product offerings, from 250nm down to 32/28nm.

#### **[Virage Logic Showcases Silicon Proven Advanced IP Solutions for TSMC Processes](#)**

##### ***Featuring SiWare(TM) Memory and SiWare(TM) Logic Solutions***

Monday, July 27, 11:00am - 11:15am, TSMC OIP Stage Booth #822

#### **[IC Design Central Partner Pavilion Stage - Low Power Design](#)**

##### ***Innovative Power Optimized Memory for Advanced Node SoC Design***

Monday, July 27, 3:00 - 3:30pm, ICDC Stage North Hall

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##### ***Featuring AEON(R) and NOVeA(R) Non-Volatile Memory Solutions***

Tuesday, July 28, 10:00am - 10:15am, TSMC OIP Stage Booth #822

#### **[DAC Management Day Track](#)**

Tuesday, July 28, 10:30am - 6:00pm, Room #131

#### **[Low-Power: Consumer Electronics' Catch-22](#)**

Tuesday, July 28, 11:30am - 12:15pm, Booth #1928

#### **[DFM - Band-Aid or Competitive Weapon?](#)**

Wednesday, July 29, 9:00am - 11:00am, Room #131

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##### ***Featuring Intelli(TM) DDR and PCIe Advanced Interface IP Product Solutions***

Wednesday, July 29, 10:00am - 10:15am, TSMC OIP Stage Booth #822

**[Chip Estimate - IP Talks! - Virage Logic Overview](#)**

Wednesday, July 29, 10:00am - 10:20am, Booth #1100 South Hall

**[Virage Logic Showcases Silicon Proven Advanced IP Solutions for TSMC Processes](#)**

***Featuring STAR(TM) Memory System and STAR(TM) Yield Accelerator Solutions***

Wednesday, July 29, 2:00pm - 2:15pm, TSMC OIP Booth #822

**About Virage Logic**

Virage Logic is a leading provider of semiconductor intellectual property (IP) for the design of complex integrated circuits. The company's highly differentiated product portfolio includes embedded SRAMs, embedded NVMs, embedded memory test and repair, logic libraries, memory development software, and interface IP solutions. As the industry's trusted semiconductor IP partner, foundries, IDMs and fabless customers rely on Virage Logic to achieve higher performance, lower power, higher density and optimal yield, as well as shorten time-to-market and time-to-volume. For further information, visit <http://www.viragelogic.com>.

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SOURCE: Virage Logic Corporation

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