



MIPS Technologies Uses Virage Logic High-Speed Memory IP To Demonstrate High Performance of New 24K™ Core Family

Customers of MIPS32® 24K Core Family Assured of Quick Time-to-Market For Performance-Hungry Applications

FREMONT and MOUNTAIN VIEW, Calif., April 5, 2004 - MIPS Technologies, Inc. (Nasdaq: MIPS) and Virage Logic Corp. (Nasdaq: VIRL) announced today that Virage Logic's Area, Speed and Power (ASAP) Memory™ High-Speed (HS) IP was used to demonstrate the industry-leading performance of the new MIPS32® 24K™ family of processor cores (see related announcement from MIPS Technologies).

"Our strategy is to ensure that 24K core customers can achieve industry leading performance in a small silicon footprint and within the constraints of tight development schedules. By offering a proven, reliable core that can incorporate industry-leading technology, such as Virage Logic's ASAP Memory product, we are helping SoC designers reach their goals in a standard environment," said Victor Peng, vice president of engineering at MIPS Technologies.

Increasingly complex System-on-Chips (SoCs) require greater amounts of memory, which present significant wafer yield issues. Using Virage Logic's silicon-proven embedded memory IP, designers can integrate small, moderate or large amounts of on-chip memory to reduce overall system cost and size, while meeting time-to-market requirements. The ASAP Memory HS product is suited for high-volume applications where high-speed, area-efficient memory implementation is a requirement.

"By going the extra mile and using Virage Logic's high-speed memory IP to help ensure the 24K cores perform as intended, MIPS Technologies is giving its customers greater leverage to quickly achieve their design goals with a significant performance and time-to-market advantage," said Jim Ensell, vice president of marketing for Virage Logic. "Virage Logic is pleased to be a part of the launch of the highest-performing synthesizable processor cores in the industry and congratulates MIPS Technologies on its 24K core family."

About ASAP Memory

The ASAP Memory product line provides the largest selection of silicon-proven embedded memories designed for testability and manufacturability. The product line comprises High-Density (HD) memories optimized for area, High-Speed (HS) memories optimized for speed, and Ultra-Low-Power (ULP) memories optimized for battery-powered and hand-held applications. To evaluate the 24K cores, MIPS Technologies used the ASAP Memory HS memory product. ASAP Memory features include:

- High-current bit cells optimized for fast signal propagation, enabling faster memories
- Super-fast X-decoder technology that selects and deselects words in parallel, resulting in very quick decoding
- Proprietary circuit optimization techniques that minimize cycle time
- Ultra-High-Speed memories that utilize very advanced features, such as pipelining, to dramatically reduce memory access time

About the MIPS32 24K Core Family

The MIPS32 24K core family, which includes the 24Kc™, 24Kc Pro, 24Kf™ and 24Kf Pro versions, offers performance from 400 to 550 MHz worst case in a 0.13 micron process, the highest frequency available in 32-bit synthesizable cores for embedded markets, while minimizing design time and reducing product costs. Tailored SoC design methodologies, an Open Core Protocol (OCP) interconnect structure, standard libraries and on-chip memories from industry-leading companies help speed time-to-market, an important advantage for a processor core suited to embedded consumer applications such as digital and interactive TVs, set-top boxes and DVD players. For more information, visit the MIPS Technologies Website or contact the company at +1 650 567 5000 or email at sales@mips.com.

About Virage Logic

Virage Logic Corporation (Nasdaq:VIRL) is a leading provider of best-in-class semiconductor IP platforms based on memory,

logic, and I/Os that are silicon-proven and production ready. Virage Logic meets market demands for cost reduction, while improving performance and reliability for fabless and integrated device manufacturer (IDM) companies focused on the consumer, communications and networking, handheld and portable, and computer and graphics markets. Virage Logic is headquartered in Fremont, California and has sales and support offices worldwide. For more information, visit www.viragelogic.com or call (877) 360-6690 toll free or (510) 360-8000.

About MIPS Technologies

MIPS Technologies, Inc. is a leading provider of industry-standard processor architectures and cores for digital consumer and business applications. The company drives the broadest architectural alliance that is delivering 32- and 64-bit embedded RISC solutions. The company licenses its intellectual property to semiconductor companies, ASIC developers and system OEMs. MIPS Technologies and its licensees offer the widest range of robust, scalable processors in standard, custom, semi-custom and application-specific products. The company is based in Mountain View, Calif., and can be reached at +1 (650) 567-5000 or www.mips.com.

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SAFE HARBOR STATEMENT FOR VIRAGE LOGIC UNDER THE PRIVATE SECURITIES LITIGATION REFORM ACT OF 1995:

Statements made in this news release other than statements of historical fact are forward-looking statements, including, for example, statements relating to Virage Logic's business outlook, new products and new relationships. Forward-looking statements are subject to a number of known and unknown risks and uncertainties, which might cause actual results to differ materially from those expressed or implied by such statements. These risks and uncertainties include Virage Logic's ability to maintain and develop new relationships with third-party foundries, adoption of technologies by semiconductor companies and increases in the demand for their products, the company's ability to overcome the challenges associated with establishing licensing relationships with semiconductor companies, the company's ability to obtain royalty revenues from customers in addition to license fees, business and economic conditions generally and in the semiconductor industry in particular, competition in the market for embedded memories and other risks including those described in the Company's Annual Report on Form 10-K for the period ended September 30, 2002, filed with the Securities and Exchange Commission (SEC) on December 16, 2002, and in Virage Logic's other periodic reports filed with the SEC, all of which are available from Virage Logic or from the SEC's website (www.sec.gov), and in press releases and other communications. Virage Logic disclaims any intention or duty to update any forward-looking statements made in this news release.

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