



September 8, 2017

Media Alert: QuickLogic to Present at D&R IP-SoC Days Shanghai

SUNNYVALE, Calif., Sept. 08, 2017 (GLOBE NEWSWIRE) -- QuickLogic Corporation (NASDAQ:QUIK), a developer of ultra-low power multi-core voice-enabled SoCs, embedded FPGA IP, display bridge and programmable logic solutions will be presenting and demonstrating its [ArcticPro eFPGA](#) solutions at the Design and Reuse IP-SoC Days in Shanghai on September 14, 2017.

Presentation

Who: Itsu Wang, Senior Director, Asia Sales and Marketing

What: Innovation & Differentiation for your SoC...with embedded FPGA Technology

This presentation will outline the reasons why eFPGA IP is an ideal solution to address IoT and other fragmented markets. With QuickLogic's ArcticPro™ eFPGA, chip designers can now easily customize their products post production to serve multiple applications.

When: September 14, 2017 at 2:00 p.m.

Where: Evergreen Laurel Hotel Shanghai, No. 1136, Zu Chongzhi Road, Shanghai

Demonstration

What: QuickLogic will demonstrate ArcticPro™ eFPGA capabilities and how it can improve overall system performance and flexibility while extending battery life and reducing product life-cycle costs.

When: September 14 from 9:00 a.m. to 5:00 p.m.

For more information, please visit

<http://www.design-reuse-embedded.com/ipsocdays/shanghai2017.jsp>

About QuickLogic

QuickLogic Corporation (NASDAQ:QUIK) enables OEMs to maximize battery life for highly differentiated, immersive user experiences with Smartphone, Wearable and IoT devices. QuickLogic delivers these benefits through industry leading ultra-low power customer programmable SoC semiconductor solutions, embedded software, and algorithms for always-on voice and sensor processing. The company's embedded FPGA initiative also enables SoC designers to easily implement post production changes, and increase revenue by providing hardware programmability to their end customers. For more information about QuickLogic, visit www.quicklogic.com.

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Press Contact:

Andrea Vedanayagam

Veda Communications

408.656.449

pr@quicklogic.com

 Primary Logo

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