



## Apache Design's RTL Power Model Technology Honored With DesignCon 2012 Award

### ANSYS Subsidiary Earns DesignVision Award for Innovative Electronics Software

PITTSBURGH, Feb. 7, 2012 /PRNewswire/ -- A technology from [Apache Design, Inc.](#) — an [ANSYS](#) (NASDAQ: ANSS) subsidiary — has earned a prestigious award celebrating innovative electronics tools that simplify customer design processes. RTL Power Model (RPM™) won DesignCon 2012's DesignVision Award in the System Modeling and Simulation Tools category. Launched in late 2011, RPM technology enables organizations to optimize a wide range of power-sensitive applications, such as ultra-low-power electronics. The product helps ensure that power-related decisions can be made with confidence early in the design flow by bridging the power gap from register-transfer-language (RTL) design to physical implementation.

(Logo: <http://photos.prnewswire.com/prnh/20110127/MM38081LOGO> )

This is the second DesignVision Award for Apache; its first was for PowerArtist™ technology in 2009.

"It is a privilege for Apache to be recognized with this award for our RPM technology, which optimizes a wide range of power-sensitive applications including ultra-low-power electronics," said Dr. Andrew Yang, president of Apache and vice president and general manager of ANSYS. "This win underscores ANSYS' and Apache's deep commitment to consistently providing best-in-class products to our customers and the IC design industry."

DesignVision award winners were selected based on three criteria: how well the product met the market's vision and offered unique insight into customer needs; the originality of the solution and if it offered a new approach to meeting market needs; and the quality of the implementation and how well it fits the market requirements. RPM accurately predicts integrated circuit (IC) power behavior at the RTL level with consideration for how the design is physically implemented. As a result, the technology helps to enable chip power delivery network (PDN) and IC package design decisions early in the design process, as well as to ensure chip power integrity sign-off for sub-28nm ICs.

#### [About Apache Design, Inc.](#)

Apache Design, an ANSYS subsidiary, enables simulation-driven IC and electronic systems design by providing advanced chip-level power analysis, optimization, and sign-off solutions. Apache's integrated products and methodologies advance low-power innovation and address chip-package-system power and noise challenges. Using Apache's engineering simulation software early in the design and throughout the process enables the world's top semiconductor companies to gain a competitive advantage delivering more power-efficient, high-performance, and noise immune chips. Apache's products help lower power consumption, increase operating performance, mitigate design risks, reduce system cost, and shorten time-to-market for a broad range of end-markets and applications. Learn more at: <http://www.apache-da.com/>.

#### **About ANSYS, Inc.**

ANSYS brings clarity and insight to customers' most complex design challenges through fast, accurate and reliable engineering simulation. Our technology enables organizations — no matter their industry — to predict with confidence that their products will thrive in the real world. Customers trust our software to help ensure product integrity and drive business success through innovation. Founded in 1970, ANSYS employs more than 2,100 professionals, many of them expert in engineering fields such as finite element analysis, computational fluid dynamics, electronics and electromagnetics, and design optimization. Headquartered south of Pittsburgh, U.S.A., ANSYS has more than 60 strategic sales locations throughout the world with a network of channel partners in 40+ countries. Visit [www.ansys.com](http://www.ansys.com) for more information.

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